

## CLAIMS

What is claimed is:

- 1 1. A computer system comprising:
  - 2 a controller to generate a power state status signal indicating the power
  - 3 states of a first plurality of components of the computer system; and
  - 4 a voltage regulator coupled to the plurality of components to increase an
  - 5 output voltage level to the first plurality of components when the first
  - 6 plurality of components enter a sleep state, as indicated by the power
  - 7 state status signal.

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